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near3 register and (NOP or no2 near2 operatis2 near instruction\$1 or null near2 instruction\$1 or nulnear2 instruction\$1 or none near operatis2] and (live or active or dead) near2 register (1/17/5.ccls. or 712/5.ccls.) and NOP and (1/17/5.ccls. or 717/151.ccls.) and (attain same live) and set34 same register) and (attain same compils5) and (dynamic same optimis6) and (register) asset bits and compils and optimis and static and projects and (NOP or no2 near2 operatis2 near instruction\$1 or nonl and compils and optimis and static and optimis and optimis and static and optimis and optimis and s		56	(717/\$,ccls, or 712/\$,ccls,) and us\$2	USPAT:	2002/06/10
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Operatis2) and (live or active or dead) No.					
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10 10 (717/s.ccls. or 712/s.ccls.) and NOP and (live or active or dead) near2 register SPO, JPO, DERWENT; IBM TDB 2002/06/10 14:13 2002/06/10 200					
14:13 2002/06/10 2002/06/	6	68		USPAT;	2002/06/10
1			(live or active or dead) near2 register	EPO; JPO;	14:13
2					
(live or active or dead) near2 register) RPO; JPO; 14:14 near5 live same register 17/7/5.ccls. or 712/5.ccls.) and NOP and (live or active or dead) same register DEWRNT; IBM TDB 2002/06/10 14:14 2002/06/10					
and (determin\$3 or find\$3 or check\$3) near5 live same register (live or active or dead) same register 10 10 (7(717/s.ccls. or 712/s.ccls.) and NOP and (live or active or dead) same register) and (determin\$3 or find\$3 or check\$3) near5 live same register ((717/s.ccls. or 712/s.ccls.) and NOP and (live or active or dead) same register) and (determin\$3 or find\$3 or check\$3) same live 10 10 ((7(717/s.ccls. or 712/s.ccls.) and NOP and (live or active or dead) same register) and (determin\$3 or find\$3 or check\$3) same live 11 5 ((7(717/s.ccls. or 712/s.ccls.) and NOP and (live or active or dead) same register) and (determin\$3 or find\$3 or check\$3) same live 11 5 ((7(717/s.ccls. or 717/s1.ccls.) and NOP and (live or active or dead) same register) and (determin\$3 or find\$3 or check\$3) same live 12 (7(717/s.ccls. or 717/151.ccls.) and (static same compil\$5) and (dynamic same optimis\$6) 13 (7(717/s.ccls. or 717/151.ccls.) and (static same compil\$5) and (dynamic same optimis\$6) and (register\$1 same bit same vectox) 14 (7(717/s.ccls. or 717/151.ccls.) and compil\$ and optimis and static and translat\$ and dead and live and bit same optimis\$6) and optimis and static and translat\$ and dead and live and bit same (instruction\$1 or code) 10 (717/s.ccls. or 717/151.ccls.) and NOP same (instruction\$1 or code) and compil\$ and static and optimis and (dynamic or runtime) 10 428 (717/s.ccls. or 712/s.ccls.) and NOP same (instruction\$1 or code) and compil\$ and static and optimis and (dynamic or runtime) 10 428 (717/s.ccls. or 712/s.ccls.) and NOP same (instruction\$1 or code) and compil\$ and static and optimis and (dynamic or runtime) 10 428 (717/s.ccls. or 712/s.ccls.) and NOP same (instruction\$1 or none near operatis\$2 nare instruction\$1 or none near operatis\$2 nare ins	7	2		,	
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		242	nears live same register		2002/06/10
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- 14 ((717/\$.ccls. or 712/\$.ccls.) and us\$2 USPAT; 2002/06/10 near3 register and (NOP or no\$2 near2 operati\$2 near instruction\$1 or null near2 instruction\$1 or none near operati\$2) and (live or active or dead)					
near3 register and (NOP or no\$2 near2 operati\$2 near instruction\$1 or null near2 instruction\$1 or none near operati\$2) and (live or active or dead)				,,,,,,,	2000/05/20
operati\$2 near instruction\$1 or null DERWENT; near2 instruction\$1 or none near operati\$2) and (live or active or dead)	-	14			
near2 instruction\$1 or none near		7 10 7			10.40
operati\$2) and (live or active or dead)				1	
				12.1. 12.5	
		4.3	near2 register) and bit near2 vector		

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